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09/13/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPLICATION	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
APPL NUM 10065045	FILING DATE 09/13/2002	CLASS 331	SUBCLASS 17	GAU 2817	EXAMINER KinKead
**APPLICANTS: Ho Hsu-Feng; Hsu Tse-Hsiang;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO MTKP0019USA	
TITLE : Phase-locked loop having phase detector error signal reshaping and method thereof					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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